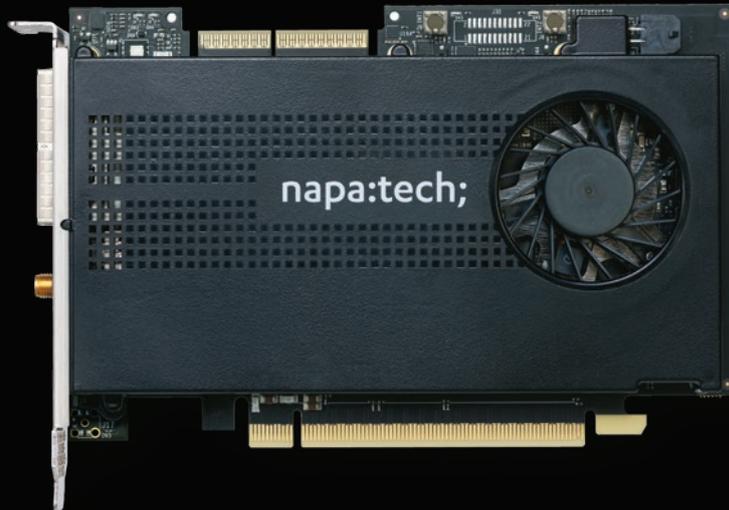


## NT400D11 PCIe4 SmartNIC with Link-Capture™ Software

2×100G

### DATA SHEET



#### Packet Capture and Replay

Use cutting-edge SmartNIC technology to add real-time line-rate performance to your application. The NT400D11 SmartNIC provides full packet capture of network data at 200 Gbps with zero packet loss. Nanosecond precision time-stamping and merge of packets from multiple ports ensures correct timing and sequencing of packets. The SmartNIC can also be used for 100% packet replay with nanosecond precision of all networking traffic for analytics, testing and simulation. The PCIe4 ×16 host interface supports full-duplex 2 × 100G packet transfer between network ports and server memory.

The NT400D11 SmartNIC enables full utilization of CPU cores through advanced receive side scaling with support for tunneling protocols, such as GTP, IP-in-IP, NVGRE and VxLAN. The NT400D11 SmartNIC can also remove duplicate packets, slice packets and filter packets to reduce the amount of data and thereby offload the server system and applications. Stateful flow processing with support for up to 140 million flows enables CPU-hungry applications, such as Suricata, to intelligently select exactly which flows to process and which to ignore. Flow records are maintained for all flows and reported to the application.

#### Applications

Napatech SmartNICs enable implementation of high-performance network appliances based on standard servers. Examples of applications include:

- Quality of experience optimization
- Financial trading latency measurement
- Customer experience analysis
- Data loss prevention
- Cyber defense
- Fraud detection and compliance management
- Infrastructure management and security
- Network and application performance
- Troubleshooting and compliance



Wireshark



Suricata



n2disk



Snort



Zeek



TRex

# FEATURE HIGHLIGHTS AND SPECIFICATIONS

## Rx Packet Processing

- Zero packet loss for packet size 64 – 10,000 bytes
  - Sustained traffic up to 200 Gbps
  - Line rate 2 × 100 Gbps traffic burst, 680 milliseconds buffering
- Multi-port packet merge, sequenced in time stamp order
- L2, L3 and L4 protocol classification
- L2 and L3/L4 (IP/TCP/UDP) checksum verification
- Tunneling support: GTP, IP-in-IP, GRE, NVGRE, VxLAN, Pseudowire, Fabric Path and VNtag
- Pattern match, network port, protocol, length, and error filters
- Custom flow definitions based on 2-, 3-, 4-, 5- or 6-tuple
- Filter and flow match/actions:
  - Forward to specific CPU queue
  - Load distribute over CPU queues
  - Forward (hairpin) to network port
  - Drop packet
  - Slice packet
  - Strip header
  - Mask packet
  - Add color/tag to packet
- Stateless flow management
  - Up to 36,000 IPv4 or up to 8,000 IPv6 addresses
- Custom hash keys, symmetric hash key option
- CPU load distribution based on hash key or filter or per flow
- 128 Rx queues, 16 MB – 1 TB Rx buffer size
- Packet descriptors with metadata
- IP fragment handling
- Deduplication
- Slicing at dynamic offset or fixed offset from start or end of packet
- Header stripping, protocol layers between outer L2 and inner L3
- Packet masking, 1 – 64 bytes at dynamic or fixed offset
- Extended RMON1 and counters per filter and per queue

## Tx Packet Processing

- Line rate Tx up to 2x100 Gbps for packet size 64 – 10,000 bytes
- Replay as captured with nanoseconds precision
- Per-port traffic shaping
- Port-to-port forwarding
- L2 and L3/L4 (IP/TCP/UDP) checksum generation
- 128 Tx queues, 4 MB Tx buffer size

## Time Stamping and Synchronization

- Rx time stamp
- Tx time stamp inject
- OS time
- Time stamp formats: Unix 10 ns, Unix 1 ns, PCAP 1 μs, PCAP 1 ns

## Network Standards

- IEEE 802.3 100G Ethernet

## Supported pluggable modules

- 100GBASE-SR4, SR-BiDi, LR4, PSM4

## Software

- Operating system: Linux
- libpcap and DPDK
- Napatech NTAPI for highest performance and advanced features
- SDK tools included in source code for debugging and prototyping and as application examples

## Hardware

- Intel® Agilex™ AGF 014 FPGA
- 12 GB DDR4 SDRAM with ECC
- PCIe Gen4 16 lanes @ 16 GT/s
- 2 × QSFP56 network ports
- SMA-F 1 PPS & 10 MHz input/output
- Stratum-3 compliant TCXO
- Flash memory with support for two boot images
- Physical dimensions: ½-length and full-height PCIe
- Weight excluding pluggable modules:
  - NT400D11-SCC: 355 g
  - NT400D11-NEBS: 350 g
- MTBF according to IEC TR 62380 (UTE C 80-810 –2004):
  - NT400D11-SCC: 317,821 hours
  - NT400D11-NEBS: 398,565 hours
- Power consumption including 100GBASE-SR4 modules and typical traffic load:
  - NT400D11-SCC: 75 W
  - NT400D11-NEBS: 75 W

## Board Management

- MCTP over SMBus
- MCTP over PCIe VDM
- PLDM for Monitor and Control
- Built-in thermal protection

## Environment for NT400D11-SCC (active cooling)

- Operating temperature: 0 °C to 45 °C (32 °F to 113 °F)
- Operating humidity: 20% to 80%

## Environment for NT400D11-NEBS (passive cooling)

- Operating temperature: –5 °C to 55 °C (23 °F to 131 °F) measured around the SmartNIC
- Operating humidity: 5% to 85%
- Altitude: < 1,800 m
- Airflow: >= 2.5 m/s

## Regulatory Approvals and Compliances

- PCI-SIG®, NEBS level 3, CE, CB, RoHS, REACH, cURus (UL), FCC, ICES, VCCI, RCM

## Orderable port speed configurations

Product	Data Rates included
NT400D11-2x100	2 × 100 Gbps